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(Not for submission under 37 CFR 1.99)

Application Number		10573733
Filing Date		2006-03-28
First Named Inventor	Leena	aerts et al.
Art Unit		2816
Examiner Name	Cox,	Cassandra F
Attorney Docket Numb	er	NL031188US1

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	1 KRISHNAPURA N ET AL., "A 5.3-GHz Programmable Divider for Hiperlan in 0.25-MUM CMOS," IEEE Journal of Solid State Circuits, IEEE Inc., New York, US, vol. 35, no. 7, July 2000.							
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